

+V5P0A
SH9520_1
OR 5%
R0603_N I

+V5P0A_TypeC_SW
1/9: Fix the IO /MB position issue----V1.2
IC
20mA

9,55 USB2_P2_WP1_OC_N R4619 OR
R0402_N I 5%
55 VBUS_EN
OCP_DET

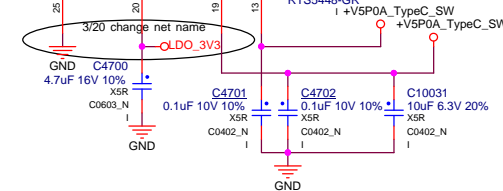
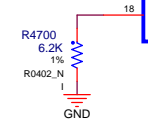
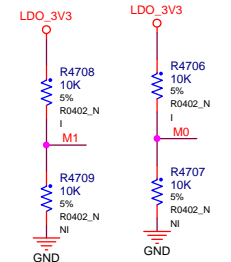
Swap P/N for USB3.1 GR2
SSRX/TX no via layout.

zdiff=90ohm
Swap P/N for USB3.1 GEN2
SSRX/TX no via layout.

9 USB3_P2_WP2_RX_DN C4530 0.1uF 10V 10% X5R C0402_N I USB3_P2_WP2_RX_C_DN 4
9 USB3_P2_WP2_RX_DP C4531 0.1uF 10V 10% X5R C0402_N I USB3_P2_WP2_RX_C_DP 5
9 USB3_P2_WP2_TX_DN C4518 0.1uF 10V 10% X5R C0402_N I USB3_P2_WP2_TX_C_DN 6
9 USB3_P2_WP2_TX_DP C4519 0.1uF 10V 10% X5R C0402_N I USB3_P2_WP2_TX_C_DP 7

GPIO4 23
M1 21
M0 22

GPIO CURRENT_M1
CURRENT_M0

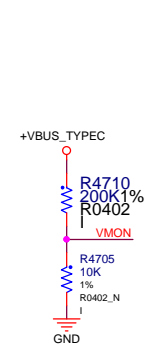


+V5P0A_TypeC_SW

Power Switch High Enable

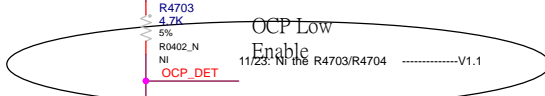
Power switch enable pin	Note
Low Active	R4701/R4702 mount
High Active	R4702 mount, R16 don't mount

For C_VBUS power switch enable pin



+V5P0A_TypeC_SW

OCP Low Enable



Power switch OCP pin	Note
Low Active	R4704/R4703 mount
High Active	R4704 mount, R4703 don't mount

For C_VBUS power switch OCP pin

	M1	M0	Note
Rp: 900mA	0	1	R11/R12 mount, R10/R13 don't mount
Rp: 1.5A	1	0	R10/R13 mount, R11/R12 don't mount
Rp: 3.0A	1	1	R10/R12 mount, R11/R13 don't mount

Rp: 1.5A (now)