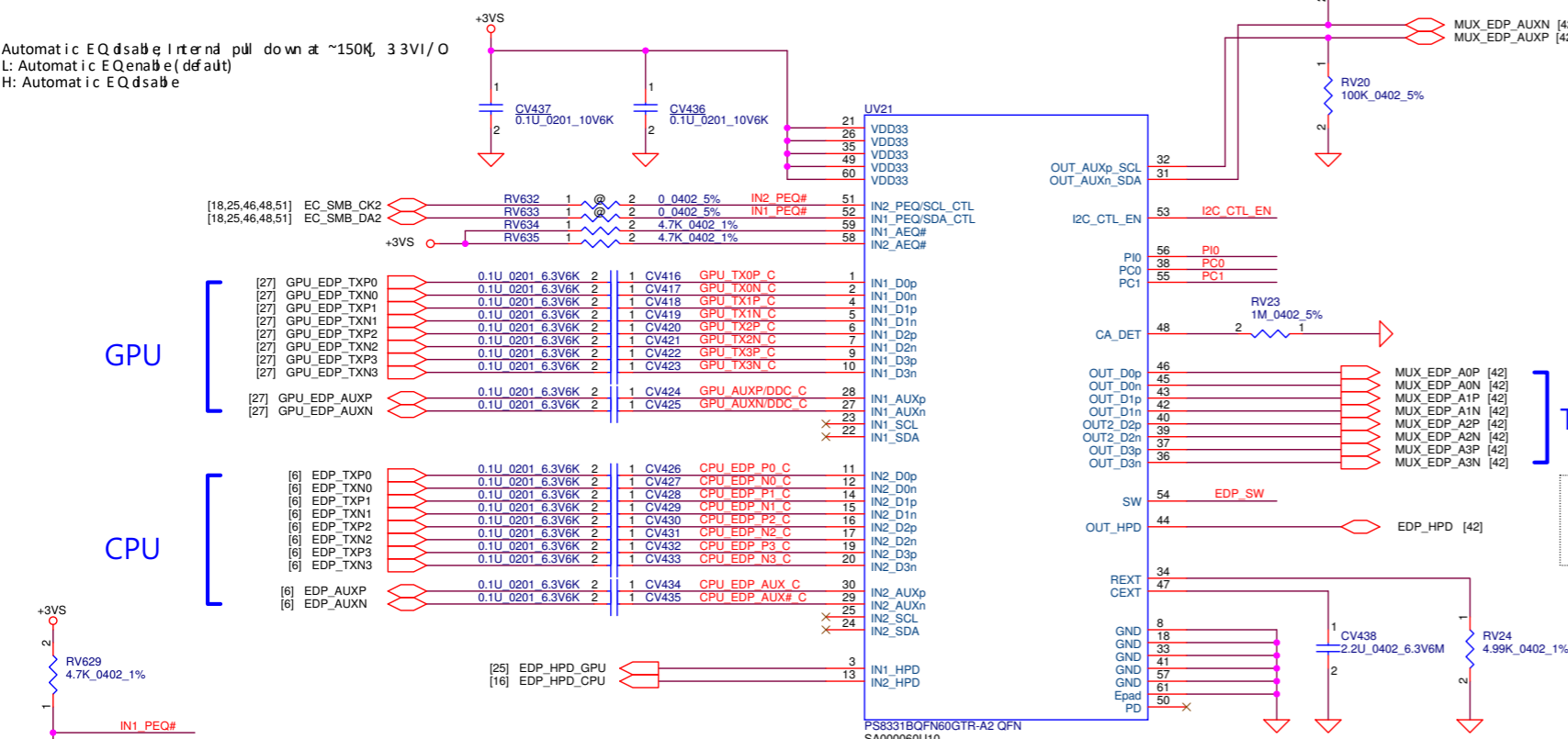


Automatic EQ disable; Internal pull down at ~150K, 3.3V I/O  
 L: Automatic EQ enable (default)  
 H: Automatic EQ disable



GPU

CPU

To eDP connector

Port switching control configuration; Internal pull down at ~150K, 3.3V I/O.  
 L: Input Port1 is selected (default)  
 H: Input Port2 is selected

I2C Control Enable; Internal pull down at ~150K, 3.3V I/O.  
 L: Pin Control is selected. (default)  
 H: I2C control is selected with default address 0x66/67  
 M: I2C control is selected with alternative address 0x D8/ D9

Auto test enable; Internal pull down at ~150K, 3.3V I/O.  
 L: Auto test disable & input of fset cancellation enable (default)  
 H: Auto test enable & input of fset cancellation enable  
 M: Auto test disable & input of fset cancellation disable

Programmable input equalization levels; Internal pull down at ~150K, 3.3V I/O  
 L: default, LEQ, compensate channel loss up to 11.5dB @ HBR2  
 H: HEQ, compensate channel loss up to 14.5dB @ HBR2  
 M: LLEQ, compensate channel loss up to 8.5dB @ HBR2

AUX interception disable for Port y (y = 1, 2). Internal pull down at ~150K, 3.3V I/O  
 L: AUX interception enable; driver configuration is set by link training (default)  
 H: AUX interception disable; driver output with fixed 80 Ohm and 0B  
 M: AUX interception disable; driver output with fixed 40 Ohm and 0B

Output swing adjustment for Port y (y = 1, 2). Internal pull down at ~150K, 3.3V I/O;  
 L: default  
 H: +20%  
 M: -16.7%

| S1 | OE | output | function |
|----|----|--------|----------|
| L  | L  | A=B1   | DGPU     |
| H  | L  | A=B2   | IGPU     |
| X  | H  |        |          |